



**RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
GROUP ART UNIT 2876**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application. No: 10/762,767
Filed: January 20, 2004
Inventor(s):
Henry Wurzburg, Tetsuo
Yamamoto, and Mark Colman
Atchison

Title: PERIPHERAL DEVICE
FEATURE ALLOWING
PROCESSORS TO
ENTER A LOW POWER
STATE

Examiner: Lee, Seung H.
Group/Art Unit: 2876
Atty. Dkt. No: 5707-06000

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Alexandria, VA 22313-1450, on the date indicated below.

Jeffrey C. Hood

9/1/2006
Date

Signature

**AMENDMENT AFTER FINAL REJECTION
RESPONSE TO OFFICE ACTION OF
JULY 14, 2006**

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of July 14, 2006, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.